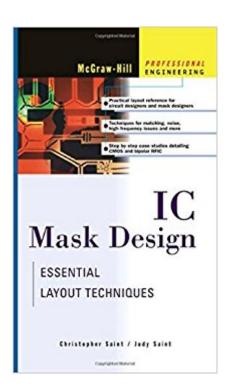


The book was found

IC Mask Design: Essential Layout Techniques





Synopsis

Integrated Circuit Mask Design teaches integrated circuit (IC) processes, mask design techniques, and fundamental device concepts in everyday language. It develops ideas from the ground up, building complex concepts out of simple ones, constantly reinforcing what has been taught with examples, self-tests and sidebars covering the motivation behind the material covered.

Book Information

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Customer Reviews

Using everyday language, the authors of this tutorial explain integrated circuit processes, mask design techniques, and fundamental device concepts. Topics include high-frequency/radio-frequency layout techniques, verification tool uses, matching and noise considerations, digital and analog layout, parasitics, floorplanning, and more. Concepts are reinforced through examples, preview points, sidebars, and self-tests. (Poptronics 2002-11-01)

THE PLAIN-ENGLISH GUIDE TO LEARNING INTEGRATED CIRCUIT MASK DESIGNAn essential reference for experienced professionals and beginners alike. Now you don $\tilde{A}\phi \hat{a} - \hat{a}, \phi t$ need an engineering background to master basic integrated circuit mask design! In this straightforward, jargon-free tutorial, co-author Chris Saint, one of IBM $\tilde{A}\phi \hat{a} - \hat{a}, \phi s$ top instructors, thoroughly covers everything from initial layout to final verification. Using clear, everyday language even a beginner

can understand, this essential primer, rich in practical techniques, covers:* Integrated circuit processes * High frequency/radio frequency layout techniques * Fundamental device concepts* Verification tool use and samples* Matching and noise considerations* Digital and analog layout* Parasitics* Floorplanning* Packaging* Key rules of thumb throughout* Two extensive case studies that comprehensively analyze complete CMOS and bipolar layout examples Utilizing a logical layered approach to teaching, Chris and Judy Saint present complicated concepts from the ground up, building on the simple until the complex becomes crystal clear. In order to reinforce key techniques, each chapter includes examples, self-tests, sidebars, preview points, \$\tilde{A}c\tilde{a} \sigma^{\tilde{A}}\tilde{motivation} \tilde{A}c\tilde{a} \sigma^{\tilde{A}}\tilde{boxes, and worked study examples \tilde{A}c\tilde{a} \sigma^{\tilde{a}} \tilde{eas well as the authors \tilde{A}c\tilde{a} \sigma^{\tilde{a}}\tilde{eas the fastest route for non-engineers to understand every phase and practical technique of IC mask design. The rules of thumb contained herein \tilde{A}c\tilde{a} \sigma^{\tilde{a}}\tilde{eas the ease the ea

good

Helped me improve my design techniques especially with interleaving.

arrive on time. great, and very happy. suit for this price. my brother need so cool product, Love! Sharp, cuts well, and feels balanced.

Clearly presented techniques with illustrations. This is the techniques companion to authors' other book, 'IC Layout Basics: A Practical Guide'. Both are an easy read, with even complex topics explained very clearly. Get both. Each has unique content from the other. Both are enjoyable reading.

The clear presentation style of this book makes for an extremely easy reading. However don't let simplicity be mistaken for usefulness. This books is packed with great advices & nice rules of thumb. Best part is the presentation of 2 step by step layout case studies: a CMOS amp & BJT mixer at the end.

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